

DEC 01 2004

Docket No.: 42P12040D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No. : 10/670,025 Confirmation No. 4529
Inventor : Damion T. Sears
Assignee : Intel Corporation
Filed : September 24, 2003
Art Unit : 2823
Examiner : Estrada, Michelle
Title : Semiconductor Device With Components Embedded in
Backside Diamond Layer

OFFICE COMMUNICATION RESPONSE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Communication mailed November 2, 2004,
reconsideration and further examination of the application is hereby requested. Kindly
consider the following:

CERTIFICATE OF TRANSMISSION

I hereby certify that I am causing the above-referenced correspondence to be facsimile transmitted to the US Patent and
Trademark Office on the date shown below:

December 1, 2004

Date of Transmission

Kenneth J. Cool

Name of Person Faxing Correspondence

Krista Mathison

Signature

December 1, 2004

Date

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INTRODUCTORY COMMENTS

Request for an Extension of Time

This Office Communication Response is submitted in response to the Office Communication dated November 2, 2004 for which the shortened statutory period for response was set to expire on December 2, 2004. It is believed that no additional extension of time fee is required. In the event any extension of time fee is required, or any other fee related to this Response is required, please consider this a request therefore. Authorization is hereby given to charge Deposit Account No. 50-0221 for such fee or fees.

Authorization to Charge Deposit Account

Authorization is hereby given to charge Deposit Account No. 50-0221 for any fee or fees related to this Office Action Response.

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